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26

PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10045376	11/09/2001	438251	247	2812	

**APPLICANTS: Doyle Brian; Roberts Brian;

**CONTINUING DATA VERIFIED:

THIS APPLICATION IS A DIV OF 09/342,030 06/28/1999 PAT 6,362,082

** FOREIGN APPLICATIONS VERIFIED: N/A

PG-PUB DO NOT PUBLISH ☐

RESCIND ☐

Foreign priority claimed ☐ yes ☒ no
35 USC 119 conditions met ☐ yes ☒ no
Verified and Acknowledged Examiners's initials DL-

ATTORNEY DOCKET NO

42390.P5768D

TITLE : Methodology for control of short channel effects in MOS transistors

U.S. DEPT. OF COMM./PAT. & TM. PTO-436L (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims 16	Print Claim for O.G. 1
ISSUE FEE		DRAWING	
Amount Due	Date Paid	Sheets Drwg. 7	Figs. Drwg. 18
		Print Fig. 18	
<input type="checkbox"/> TERMINAL DISCLAIMER		Applicant Examiner	
		PREPARED FOR ISSUE	
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